

**REMARKS**

Claims 1-7 and 9 are pending in this application. By this Amendment, claim 1 is amended for clarity, claims 2-4 are amended to correspond to the amendments to claim 1, claims 13 and 14 are canceled, and the specification is amended to correct an informality. No new matter is added by this Amendment. Support for the features added to claim 1 is found at, for example, Figs. 5A, 5B, 6, 11A and 11B.

**I. Specification**

The disclosure is objected to for an informality at page 5, line 27 of the specification. The specification is amended to cure the informality. Withdrawal of the objection is requested.

**II. Rejection Under 35 U.S.C. §112, second paragraph**

Claims 1-7 and 9 are rejected under 35 U.S.C. §112, second paragraph, as being indefinite. Independent claim 1 is amended for clarity.

In particular, the Office Action asserts that claim 1 is indefinite because claim 1 requires the second bus to have no connection to any device, and the interface circuit performs interface processing with a device connected to the second bus. The Office Action further questions how the rewriter can begin processing as no connection of the second bus to the rewriter appears to be recited.

Claim 1 is herein amended to recite that the interface circuit is connectable to a second bus, the second bus being connectable to a second device, that the rewriter loads and writes information transferred from the first device through the first bus into a rewrite area of the non-volatile memory when the rewriter is activated by the rewriter activation section, and that when the second bus is detected to have a connection to the second device, the circuit performs packet transfer through the first bus conforming to a first interface standard and the

interface circuit performs interface processing with the second device conforming to a second interface standard.

Accordingly, claim 1 as amended, overcomes the rejection of claims 1-7 and under 35 U.S.C. §112, second paragraph. Withdrawal of the rejection is requested.

**III. Rejection Under 35 U.S.C. §102(b)**

Claims 1-7, 9, 13 and 14 are rejected under 35 U.S.C. §102(b) over U.S. Patent No. 5,978,591 (Bartholomew). This rejection is respectfully traversed.

Claims 13 and 14 are canceled. Thus, the rejection of claims 13 and 14 is moot.

Bartholomew discloses that reprogramming data from the host computer (PC) is downloaded to the dual port memory 22 through a first bus at PCMCIA16. Subsequently, upon the disconnection of the host computer from the first bus, installation of downloaded reprogramming data starts. See col. 7, lines 26-48 and Fig. 3 of Bartholomew.

According to Bartholomew, the detection of whether or not the host computer has been disconnected from the first bus occurs after the reprogramming data has been downloaded through the first bus.

Claim 1 recites a rewriter activation section, the rewriter activation section activating the rewriter to start processing when the second bus is detected to have no connection to any device, the rewriter loading and writing information transferred from the first device through the first bus into a rewrite area of the non-volatile memory when the rewriter is activated by the rewriter activation section, when the second bus is detected to have a connection to the second device, the circuit performing packet transfer through the first bus conforming to a first interface standard and the interface circuit performing interface processing with the second device conforming to a second interface standard.

Thus, according to claim 1, the detection of whether or not the device is connected to the second bus occurs before information is downloaded through the first bus. Whereas,

according to Bartholomew, the detection of whether or not the host computer has been disconnected from the first bus occurs after the reprogramming data has been downloaded through the first bus.

For at least the foregoing reason, Bartholomew fails to anticipate the subject matter of claim 1, as well as the claims depending therefrom.

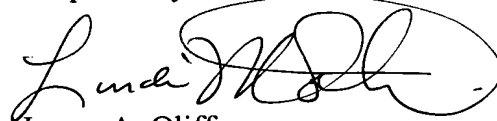
Withdrawal of the rejection is requested.

**IV. Conclusion**

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of the pending claims are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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Attachment:  
Request for Continued Examination

Date: August 22, 2006

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